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**ATTN: LEGAL DEPARTMENT**  
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Pat Slaback	(Depositor's name)
<i>Pat Slaback</i>	(Signature)
June 14, 2004	(Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/092,051	03/04/2002	Stephen M. Douglass	X-923 US	7802

TITLE OF INVENTION: METHOD OF DESIGNING INTEGRATED CIRCUIT HAVING BOTH CONFIGURABLE AND FIXED LOGIC CIRCUITRY

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1330	\$0	\$1330	06/18/2004

EXAMINER	ART UNIT	CLASS-SUBCLASS
WHITMORE, STACY	2812	716-018000

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).

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2. For printing on the patent front page, list (1) the names of up to 3 registered patent attorneys or agents OR, alternatively, (2) the name of a single firm (having as a member a registered attorney or agent) and the names of up to 2 registered patent attorneys or agents. If no name is listed, no name will be printed.

1. Garlick & Harrison
2. Kim Kanzaki
3. \_\_\_\_\_

## 3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type)

PLEASE NOTE: Unless an assignee is identified below, no assignee data will appear on the patent. Inclusion of assignee data is only appropriate when an assignment has been previously submitted to the USPTO or is being submitted under separate cover. Completion of this form is NOT a substitute for filing an assignment.

(A) NAME OF ASSIGNEE

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

XILINX, INC.

San Jose, California 95124

Please check the appropriate assignee category or categories (will not be printed on the patent): ☐ individual ☒ corporation or other private group entity ☐ government

## 4a. The following fee(s) are enclosed:

- ☒ Issue Fee
- ☐ Publication Fee
- ☐ Advance Order - # of Copies \_\_\_\_\_

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**MAIL STOP ISSUE FEE**  
**COMMISSIONER FOR PATENTS**  
**P. O. Box 1450**  
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**22313-1450**



**Inventor(s):** Stephen M. Douglass

**Assignee:** XILINX, INC.

**Serial No.:** 10/092,051

conf.no. 7802

**Filed:** March 4, 2002

**Title:** Method of Designing Integrated Circuit Having Both Configurable and Fixed Logic Circuitry

**Docket No.:** X-923 US

**Enclosed:** Return Receipt Postcard

Fee Transmittal - Part B

**Date:** June 14, 2004

**VIA FACSIMILE** 703-746-4000

**Atty/Sec:** KK/pbs